

REMARKS

Applicants will address each of the Examiner's objections and rejections in the order in which they appear in the Final Rejection. As Applicants are merely making the same amendment to the Abstract as made in the previous amendment, it is respectfully submitted that this amendment should be entered and considered by the Examiner at this time.

Specification

In the Final Rejection, the Examiner again objects to the Abstract and requests that "reduces" be changed to "reduced." Applicants previously amended the abstract as requested by the Examiner and are amending it again. Accordingly, it is respectfully request that this objection be withdrawn.

Claim Rejections - 35 USC §103

Claims 13, 16, 17, 20, 21 and 24

The Examiner also rejects Claims 13, 16, 17, 20, 21 and 24 under 35 USC §103(a) as being unpatentable over Ogawa et al. (US 6,362,507) in view of Jacobson et al. (US 6,294,401). This rejection is respectfully traversed.

Independent Claims 13, 17 and 21 recite forming a pixel electrode over the substrate by discharging a second conductive material (Claim 13); forming a plurality of pixel electrodes arranged in a matrix form over the substrate by discharging a second conductive material (Claim 17); and forming a plurality of pixel electrodes arranged in a matrix form over the substrate by discharging a second conductive material (Claim 21). By forming the pixel electrode by the claimed discharging process, the pixel electrode can be selectively formed. As a result, a photolithography step is not required after forming the pixel electrode, and the manufacturing cost for a semiconductor device can

be reduced, manufacturing time can be shortened, and manufacturing steps simplified.

In contrast, in the Final Rejection, the Examiner admits that Ogawa discloses that the pixel electrode is formed by sputtering instead of by discharging conductive material. The Examiner, however, contends that Jacobson teaches forming “the gate electrode, the source/drain wirings and additional circuit components by discharging material comprising conductive nanoparticles.”

Jacobson, however, does not disclose or suggest forming a pixel electrode by discharging conductive material, as recited in independent Claims 13, 17 and 21. Hence, the cited references do not disclose or suggest the claimed method.

While the Examiner contends that this feature would have been obvious in view of the disclosure in Jacobson, Applicants disagree. Instead, Applicants respectfully submit that it is not proper to apply the discharging method of Jacobson for forming the gate electrode and the source/drain electrode to a process for forming a pixel electrode, and therefore, it is not proper to apply the discharging method of Jacobson to the formation of the pixel electrode of Ogawa.

Further, it is respectfully submitted that there is no motivation to combine Ogawa and Jacobson. Hence, the combination of references to arrive at the claimed invention is improper, and the rejection based thereon is improper.

Accordingly, it is respectfully submitted that independent Claims 13, 17 and 21 and those claims dependent thereon are patentable over the cited references, and it is requested that this rejection be withdrawn.

Claims 14, 15, 18, 19, 22 and 23

The Examiner also rejects Claims 14, 15, 18, 19, 22 and 23 under 35 USC §103(a) as being unpatentable over Ogawa et al. in view of Jacobson et al. and further in view of Speakman (US

6,713,389). This rejection is also respectfully traversed.

Each of these claims is a dependent claim. Therefore, for at least the reasons discussed above for the independent claims, these claims are also patentable over the cited references. Accordingly, it is respectfully requested that this rejection be withdrawn.

Claims 25, 28-32, 35-39, 42-46 and 49-52

The Examiner also rejects Claims 25, 28-32, 35-39, 42-46 and 49-52 under 35 USC §103(a) as being unpatentable over Yamazaki et al. (JP 2001-052864) in view of Jacobson et al. This rejection is also respectfully traversed.

Independent Claims 25, 32, 39 and 46 recite forming a first electrode over the source and drain wirings by discharging a third conductive material (Claims 25 and 32); and forming a plurality of first electrodes arranged in a matrix form over the plurality of source and drain wirings by discharging a third conductive material (Claims 39 and 46).

In contrast, while Yamazaki discloses forming an electroluminescent layer by using a discharging method, Yamazaki discloses forming the gate and wiring layers by sputtering. Hence, Yamazaki does not disclose or suggest forming a first electrode or plurality of first electrodes by using a discharging method as in independent Claims 25, 32, 39 and 45. Jacobson also does not disclose or suggest forming a first electrode by using a discharging method. Hence, the cited references do not disclose or suggest the claimed method.

Further, Applicants respectfully submit that it is not proper to apply the discharging method of Jacobson for forming the gate electrode and the source/drain electrode to a process for forming a first electrode, and therefore, it is not proper to apply the discharging method of Jacobson to the method of Yamazaki to arrive at the claimed method.

Furthermore, it is respectfully submitted that there is no motivation to combine Yamazaki and Jacobson. Therefore, the combination of references to arrive at the claimed invention is improper, and the rejection based thereon is improper.

Accordingly, it is respectfully submitted that independent Claims 25, 32, 39 and 42 and those claims dependent thereon are patentable over the cited references, and it is requested that this rejection be withdrawn.

Conclusion


Accordingly, it is respectfully submitted that the present application is in a condition for allowance and should be allowed.

If any fee should be due for this amendment, please charge our Deposit Account 50/1039.

Favorable reconsideration is earnestly solicited.

Respectfully submitted,

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Mark J. Murphy
Registration No. 34,225

COOK, ALEX, McFARRON, MANZO,
CUMMINGS & MEHLER, LTD.
200 West Adams Street
Suite 2850
Chicago, Illinois 60606
(312) 236-8500
Customer no. 000026568